

8

7

6

5

4

3

2

1

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV

ZONE

ECN

DESCRIPTION OF CHANGE

CK APPD

DATE

ENG APPD

DATE

?

?

SCHEM, LIO, MBP 15 "

04/20/07

PDF PAGE

CSA PAGE

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SYNC MASTER

DATE

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AUDIO:CODEC

LENGO_M75_AUDIO

03/20/2007

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LENGO_M75_AUDIO

03/20/2007

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AUDIO:SPEAKER AMP

LENGO_M75_AUDIO

03/20/2007

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03/20/2007

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DC-In & Battery Connectors

(Master)

(Master)

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5V/3.3V SUPPLY

<MASTER>

<MASTER>

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PBus Supply & Batt. Charger

(Master)

(Master)

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Inverter Connector

SPLIT_INVERTER

12/06/2006

19

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Inverter Support

SPLIT_INVERTER

01/31/2007

20

99

Inverter Control IC

SPLIT_INVERTER

01/31/2007

PART#

QTY

DESCRIPTION

REFERENCE DESIGNATOR(S)

CRITICAL

BOM OPTION

051-7226

1

SCHEM,BANDCAMP,M75

SCH1

CRITICAL

820-2102

1

PCBF,BANDCAMP,M75

PCB1

CRITICAL

DIMENSIONS ARE IN MILLIMETERS

XX :

X.XX :

X.XXX :

ANGLES :

DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER

ENG APPD

QA APPD

RELEASE

DESIGN CK

MFG APPD

DESIGNER

SCALE

SIZE

NONE

MATERIAL/FINISH NOTED AS APPLICABLE

D

Apple Computer Inc.

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TITLE

SCHEM, LIO, MBP 15

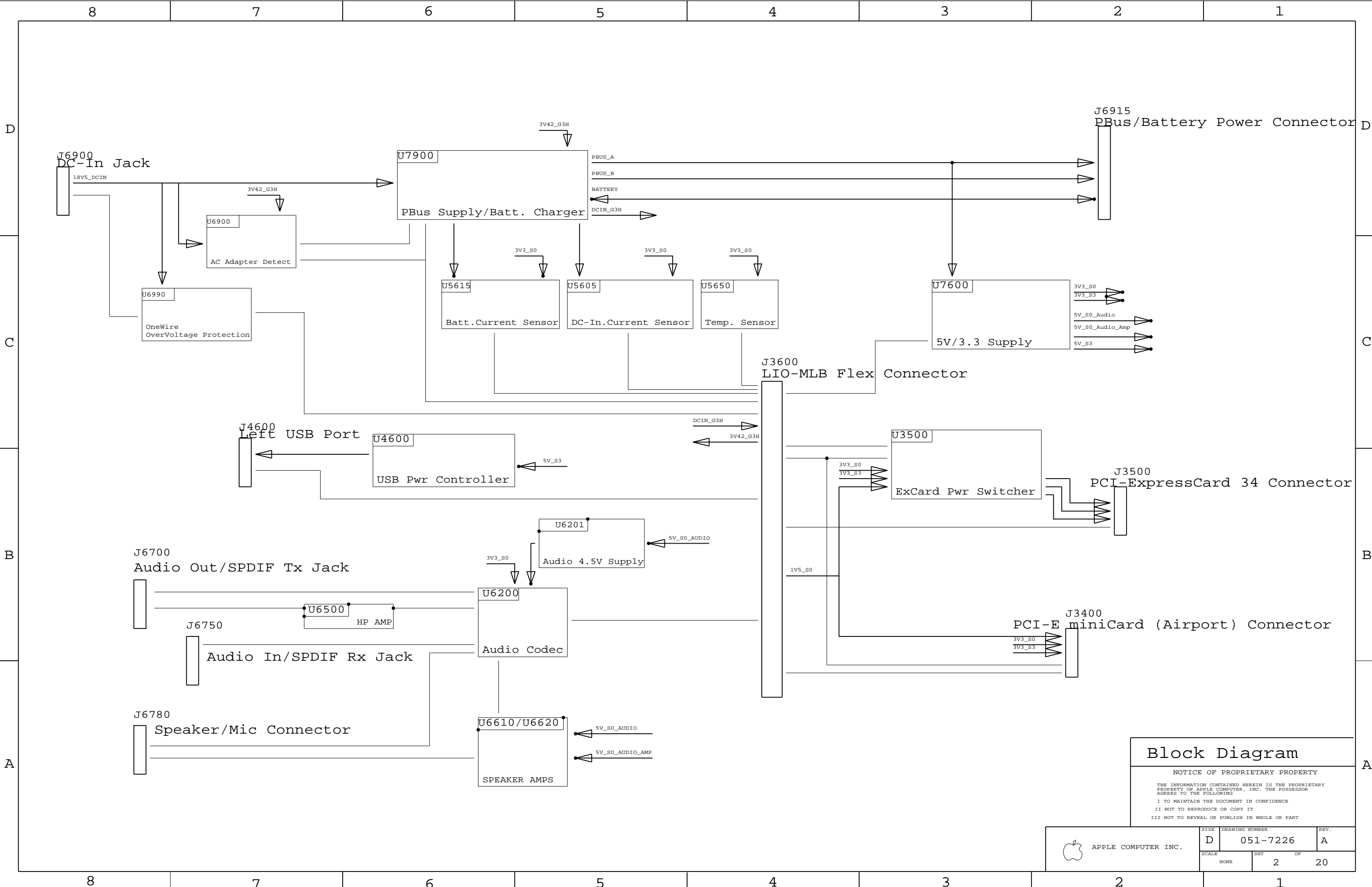
DRAWING NUMBER

051-7226

REV.

A

SHT 1 OF 20

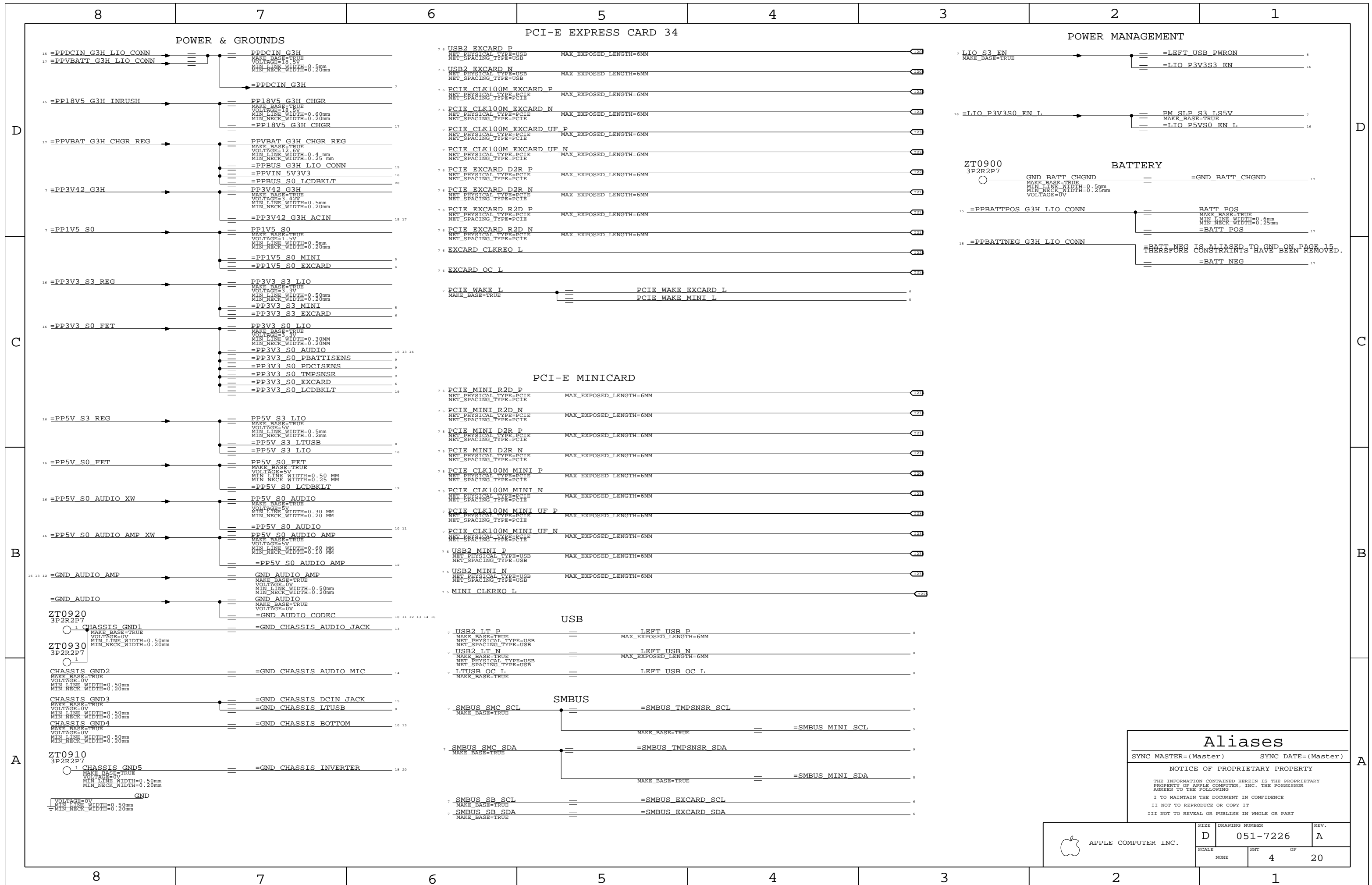


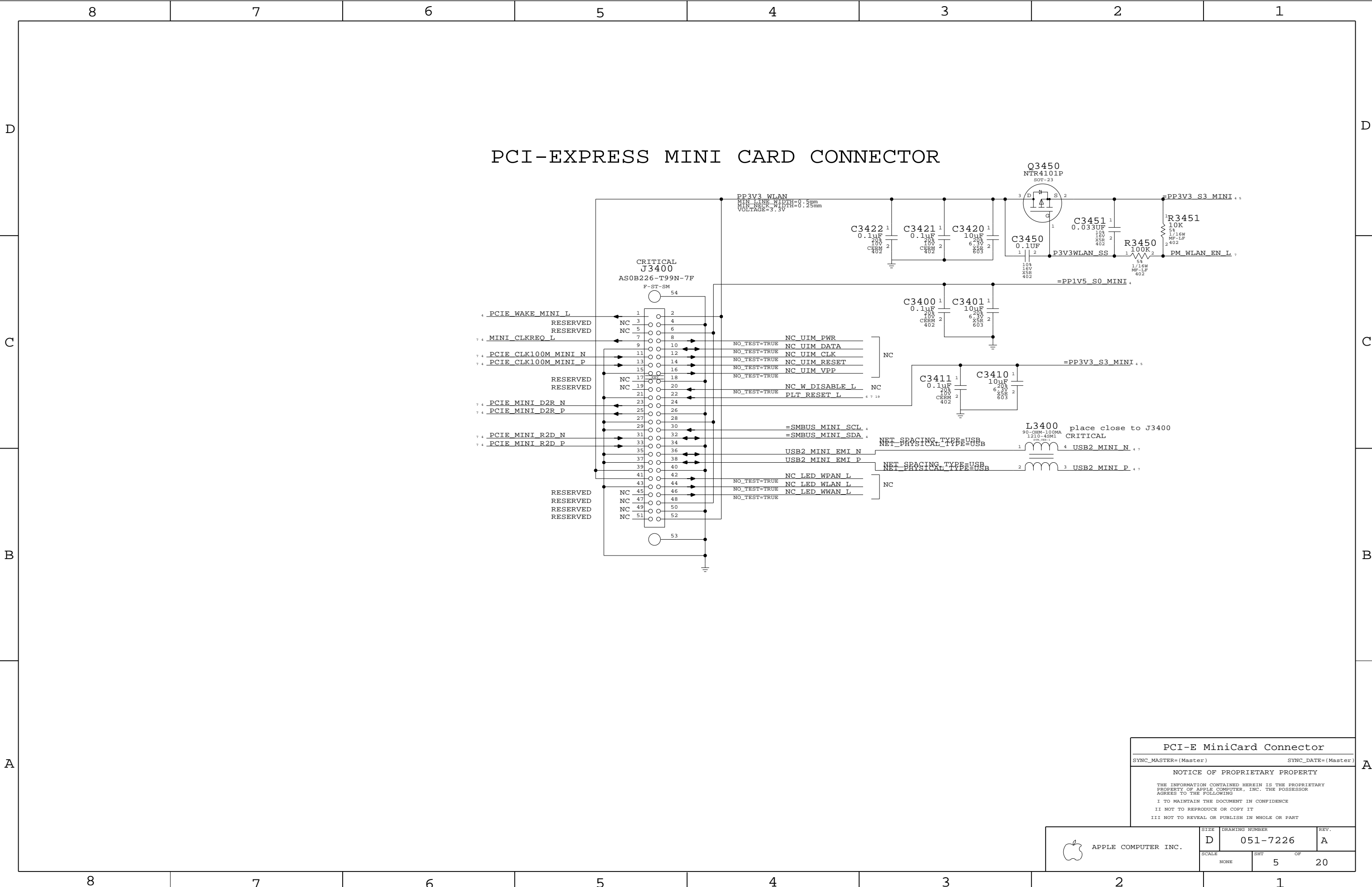
Block Diagram

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	D	051-7226		A
SCALE		SHT	OF	
NONE		2	20	

[illegible]





PCI-E MiniCard Connector

SYNC_MASTER=(Master) SYNC_DATE=(Master)

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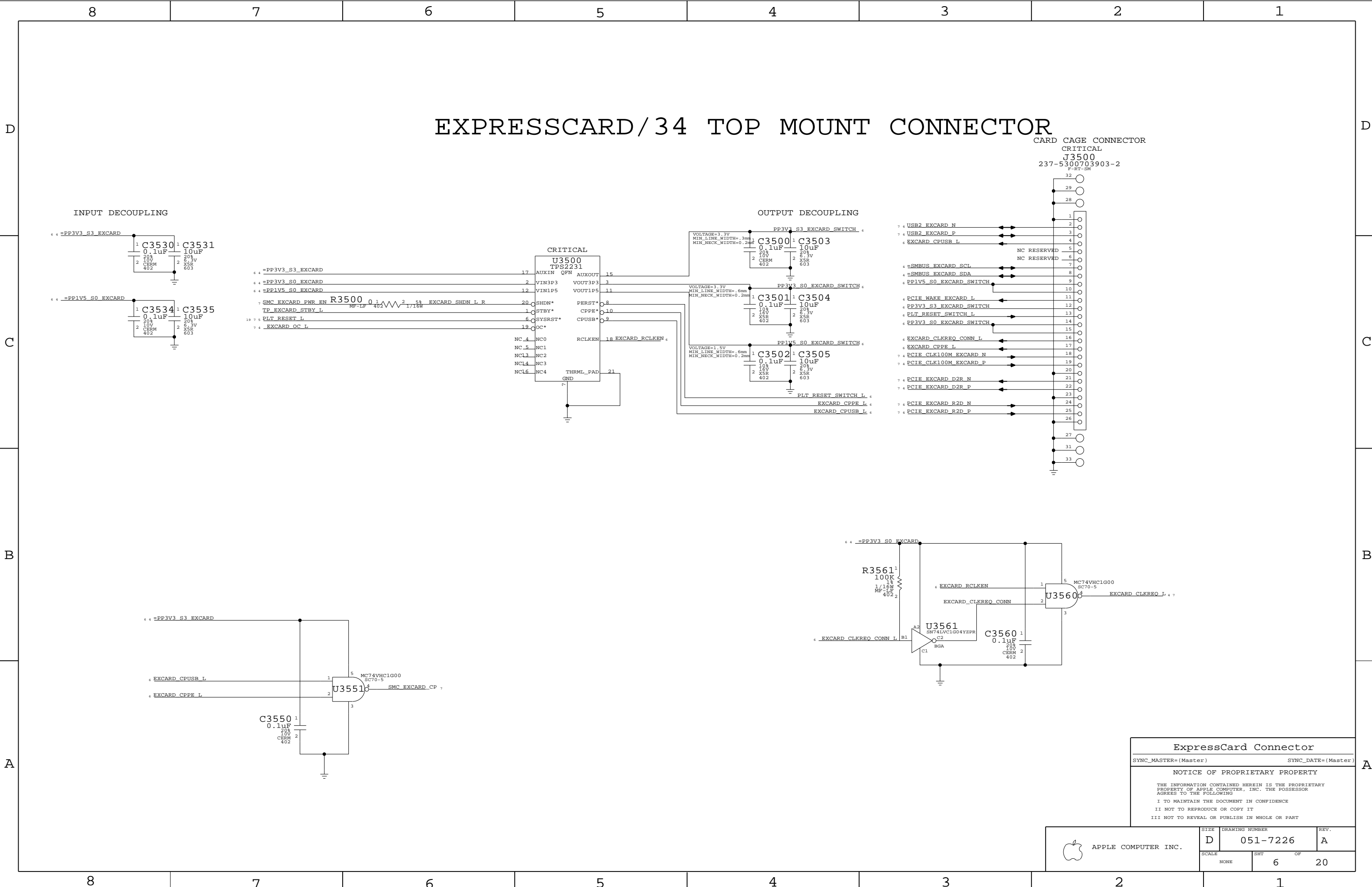
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SCALE		SHT	OF
NONE		5	20



ExpressCard Connector

SYNC_MASTER=(Master) SYNC_DATE=(Master)

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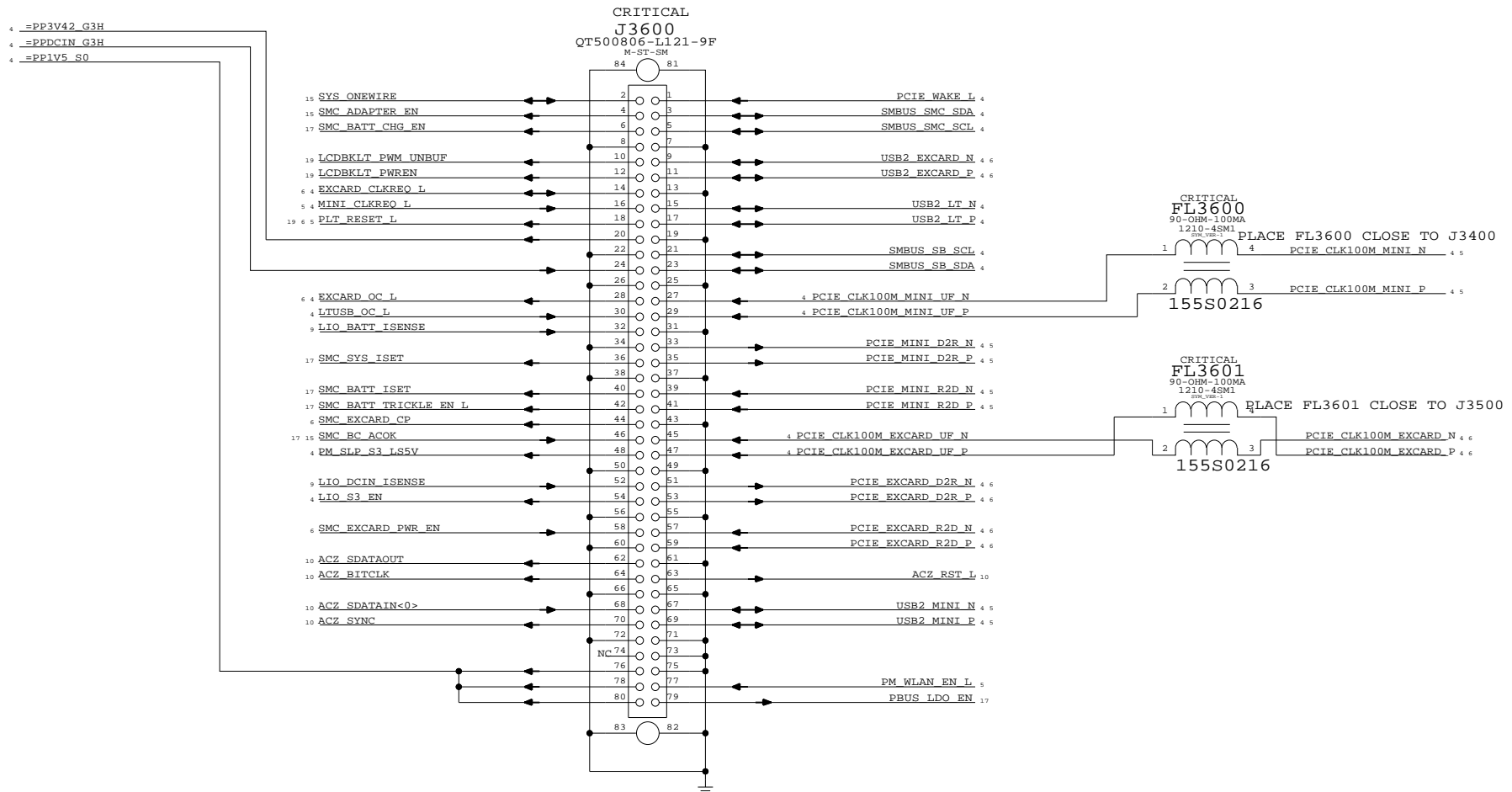
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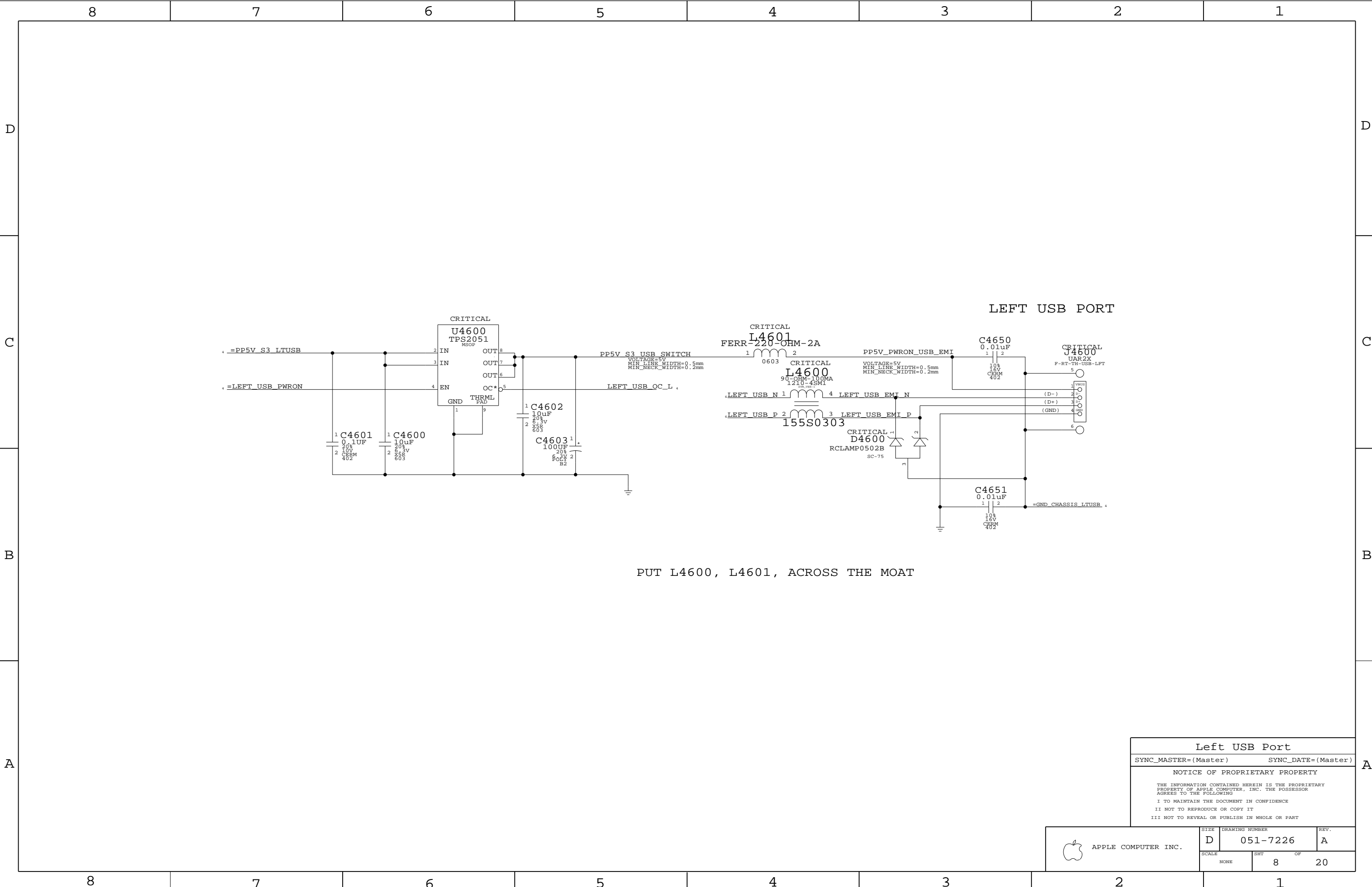
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SCALE		SHT	OF
NONE		6	20



 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7226	A
	SCALE	SHT	OF
	NONE	7	20



PUT L4600, L4601, ACROSS THE MOAT

Left USB Port

SYNC_MASTER=(Master)

SYNC_DATE=(Master)


NOTICE OF PROPRIETARY PROPERTY

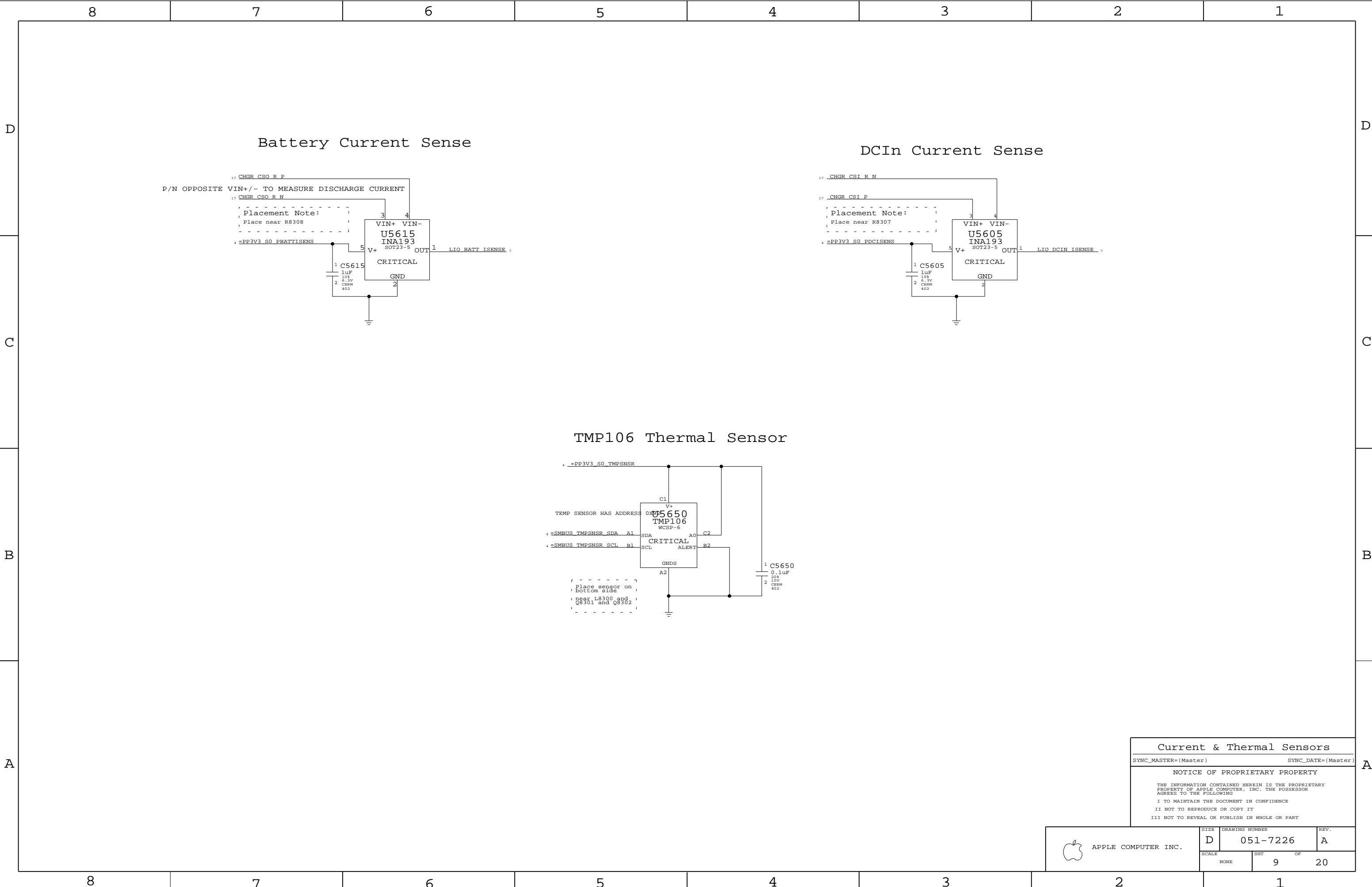
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SCALE		SHT	OF	
NONE		8	20	



D

C

B

A

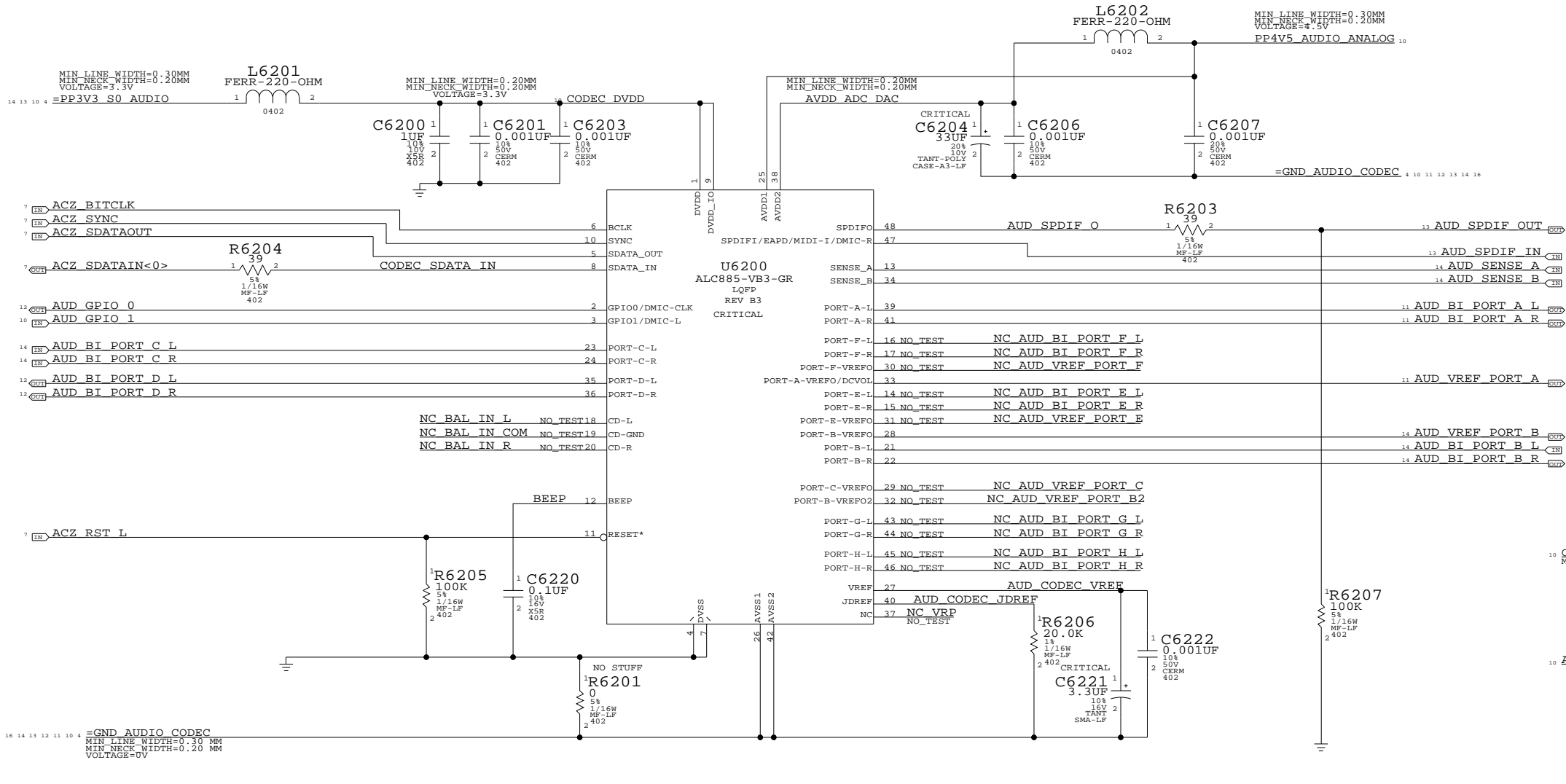
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C

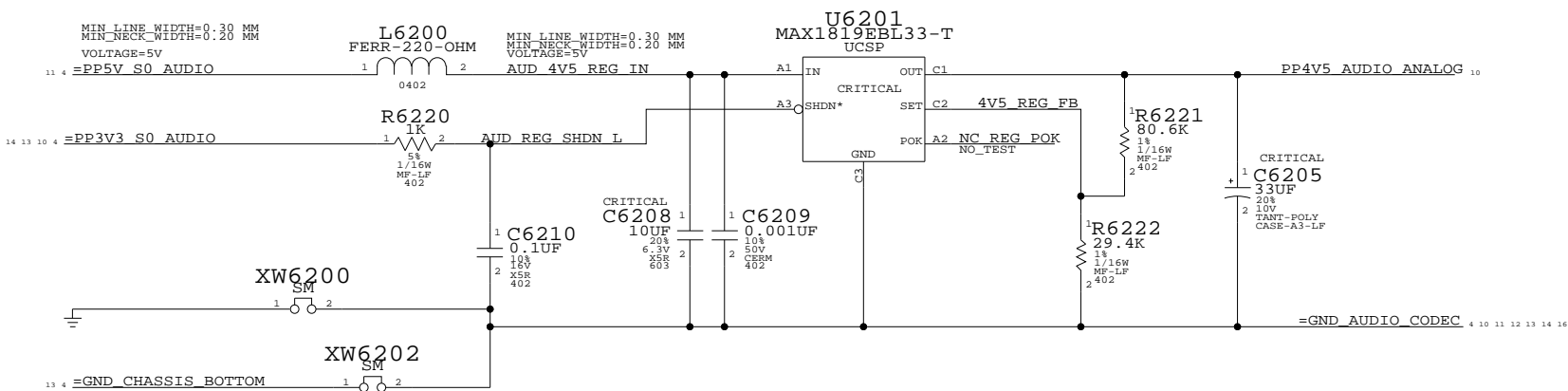
B

A

AUDIO CODEC
APPLE P/N 353S1527



AUDIO 4.5V REGULATOR
APPLE P/N 353S1455



AUDIO:CODEC

SYNC_MASTER=LENGO_M75_Async DATE=03/20/2007

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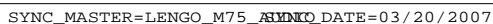
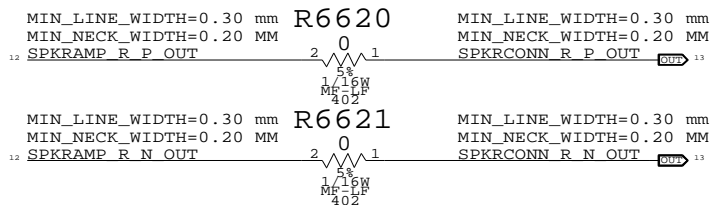
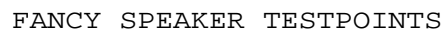
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7226	A
SCALE	SHT	OF
NONE	10	20

The schematic diagram illustrates the MAX9722A audio amplifier circuit. Key components and connections include:

- MAX9722A IC:** The central component, configured with various pins connected to power, ground, and signal lines.
- MAX9722 C1P and C1N:** Complementary output drivers connected to the MAX9722A IC.
- Passive Components:**
 - Capacitors:** C6500 (100µF), C6501 (0.001µF), C6502 (100µF), C6503 (100µF), and C6504 (100µF) are used for filtering and decoupling.
 - Resistors:** R6500 (100K), R6513 (5K), R6514 (2.21K), R6522 (2.21K), and R6523 (5K) are used for signal conditioning and biasing.
 - Inductors:** L6500 and L6501 (FERR-120-OHM-1.5A and FERR-1000-OHM) are used for power supply filtering.
- Power and Grounding:** The circuit is powered by a 5V supply (PP5V_AUDIO_HPAMP_PVDD) and has a ground reference (GND_AUDIO_CODEC).
- Signal Connections:** The input is connected to AUD_HPAMP_INL_M and AUD_HPAMP_INR_M. The output is connected to AUD_HPAMP_OUTL_R and AUD_HPAMP_OUTR_R.

NONE	11	20
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$$79\text{Hz} < \text{FC} < 132\text{Hz}$$


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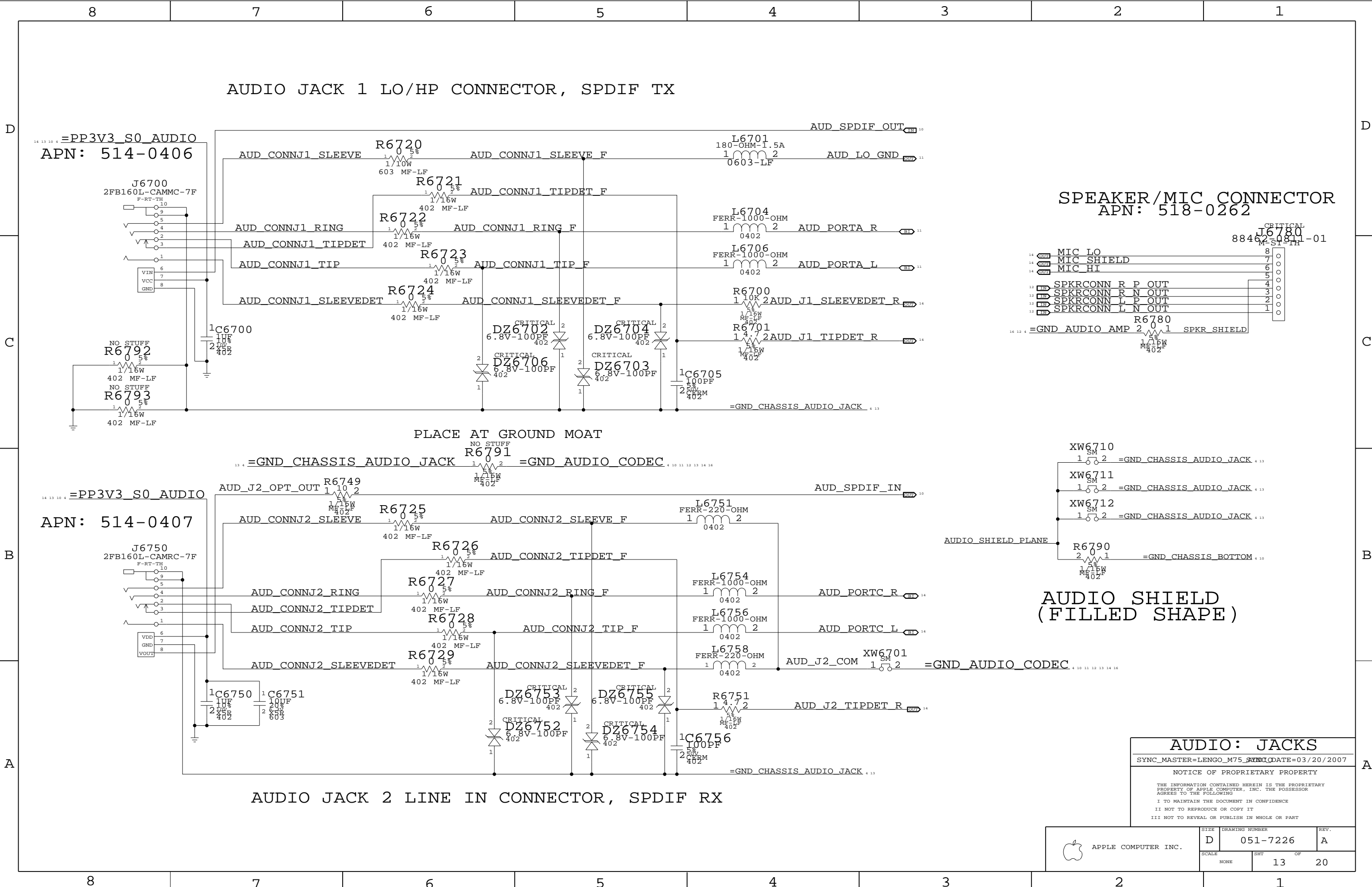
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SIZE	DRAWING NUMBER	REV.
D	051-7226	A
SCALE	SHT	OF
NONE	12	20



AUDIO: JACKS
SYNC_MASTER=LENGO_M75_SYNC DATE=03/20/2007

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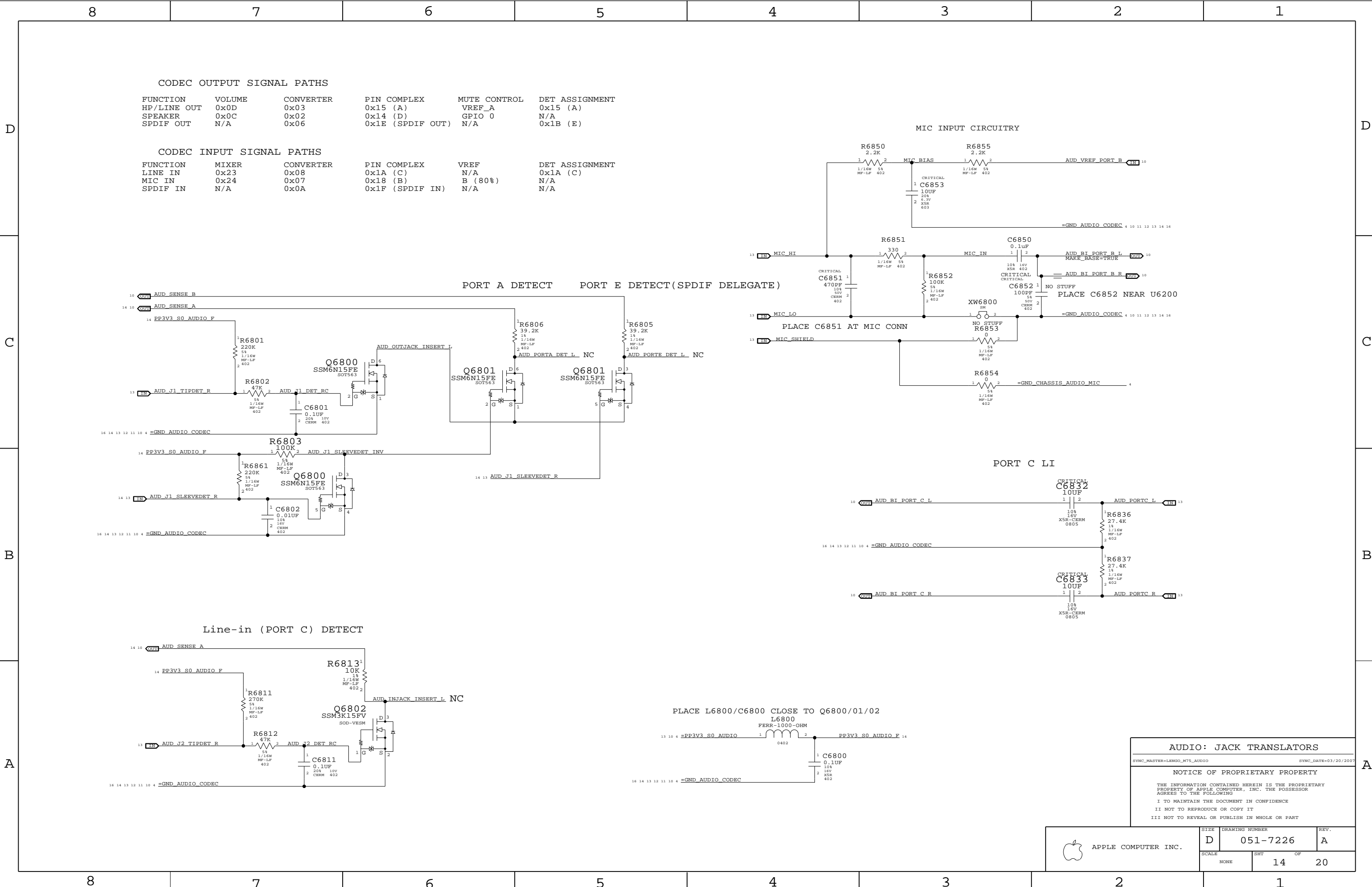
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SCALE		SHT	OF
NONE		13	20



D

D

C

C

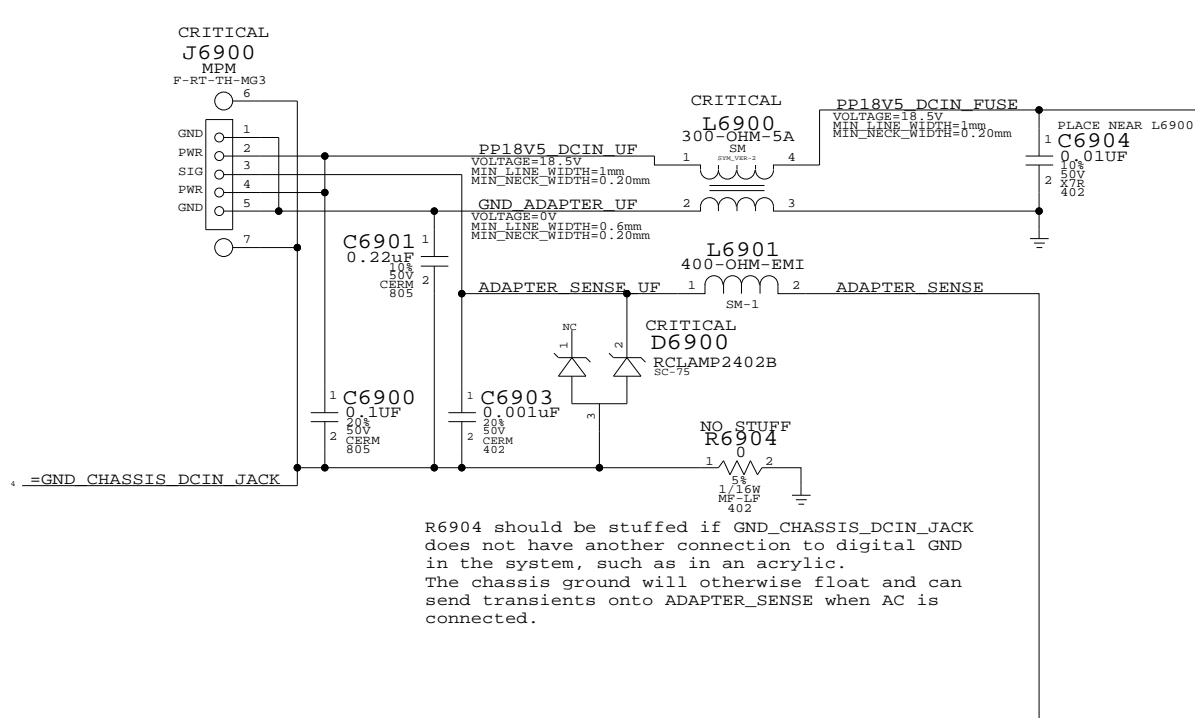
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B

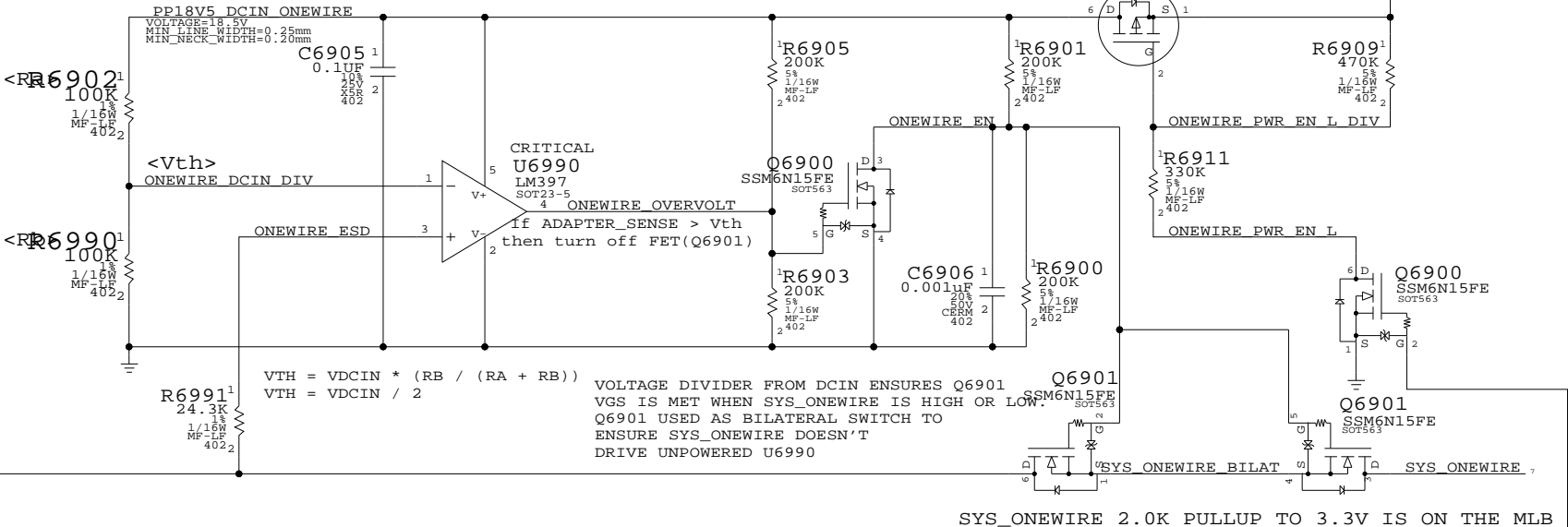
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A

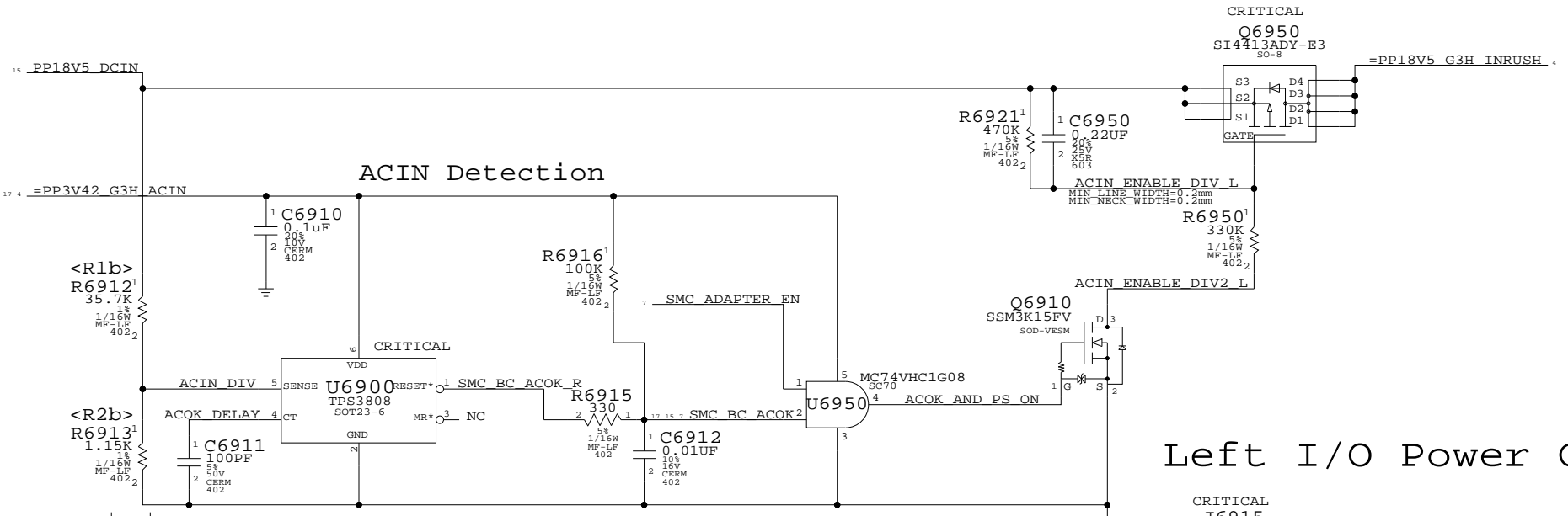
DC Power Jack



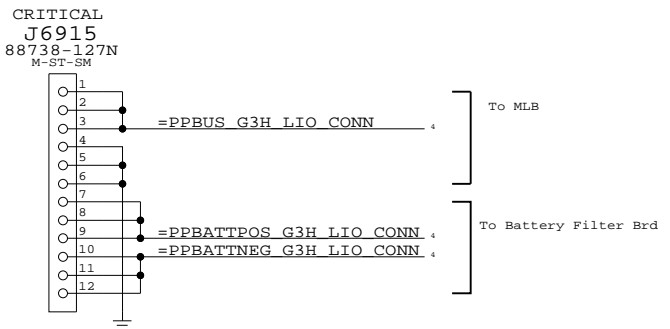
1-Wire OverVoltage Protection



Inrush Limiter

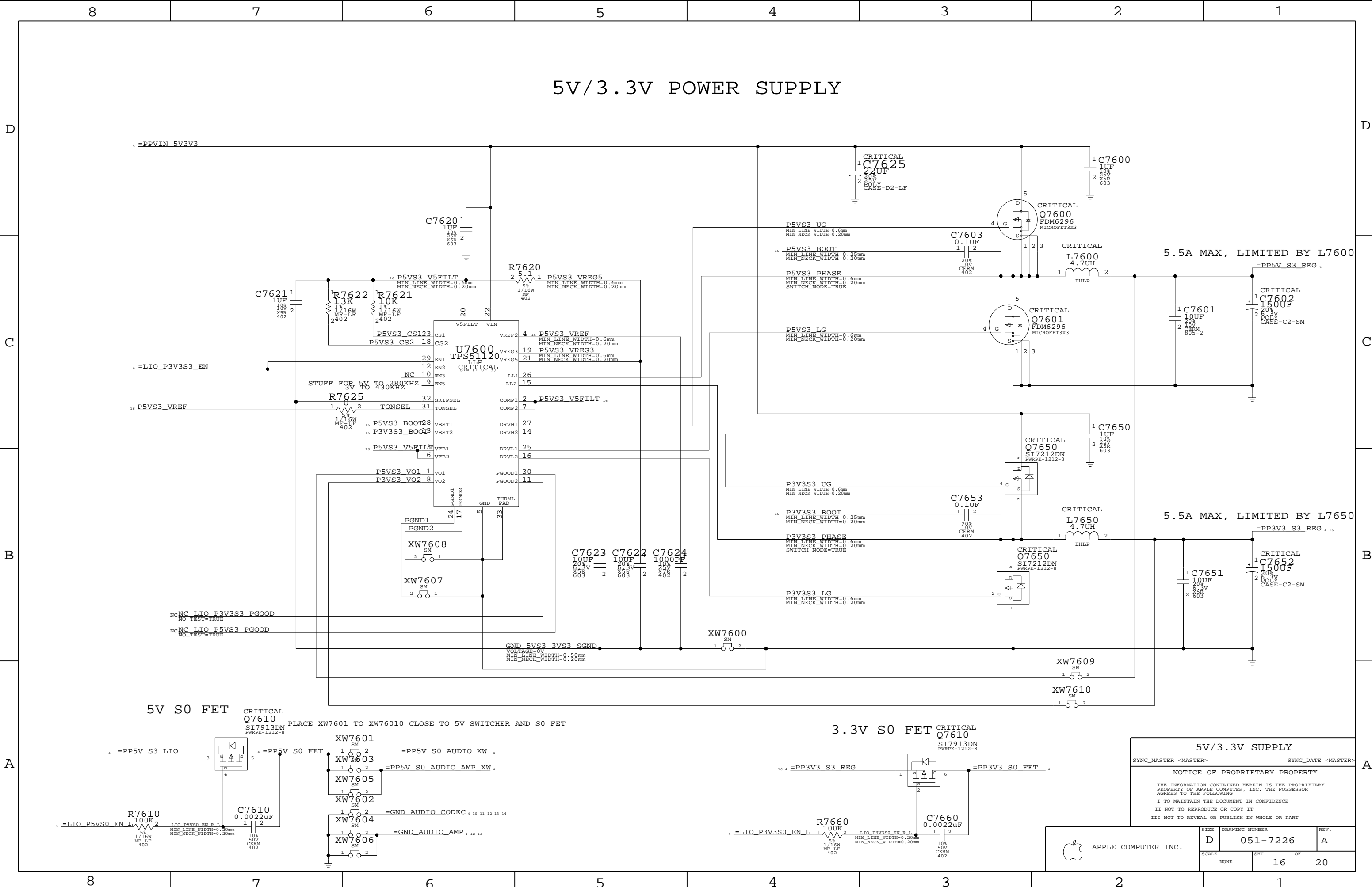


Left I/O Power Connector

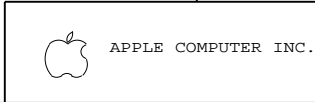


DC-In & Battery Connectors	
SYNC_MASTER=(Master)	SYNC_DATE=(Master)
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
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SCALE		SHT	OF
NONE		15	20



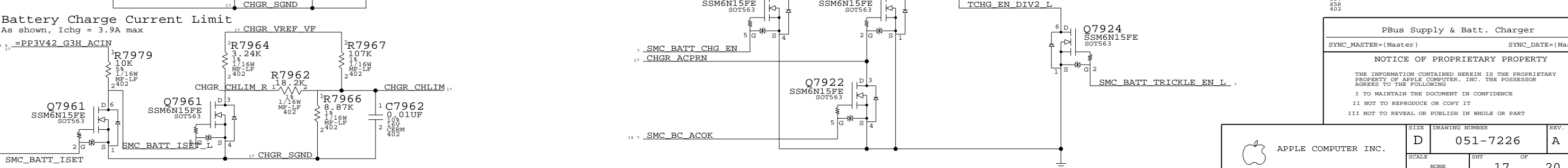
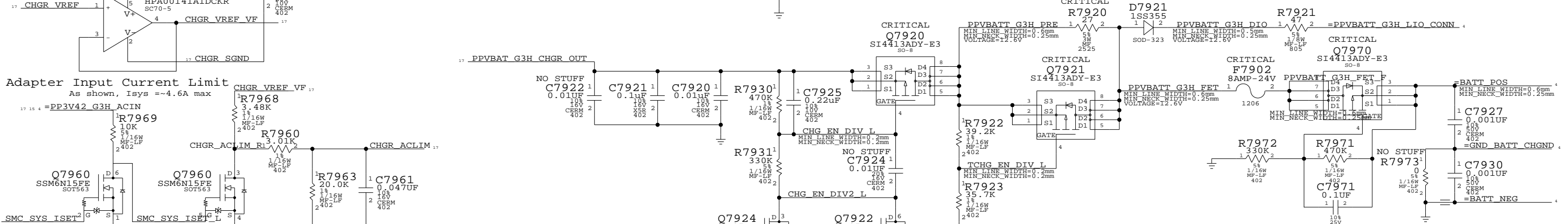
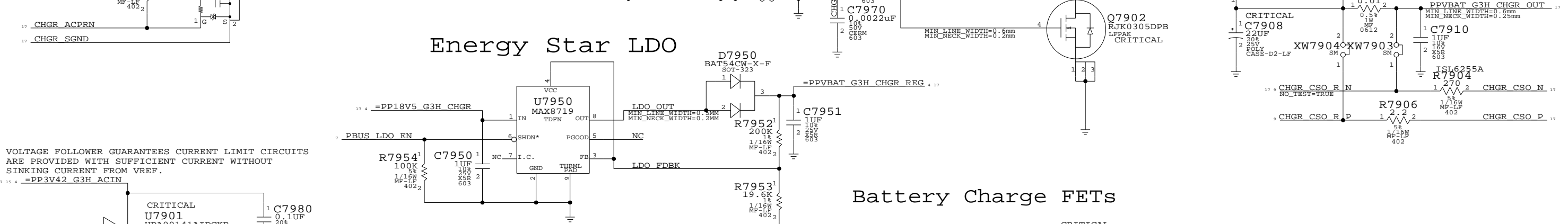
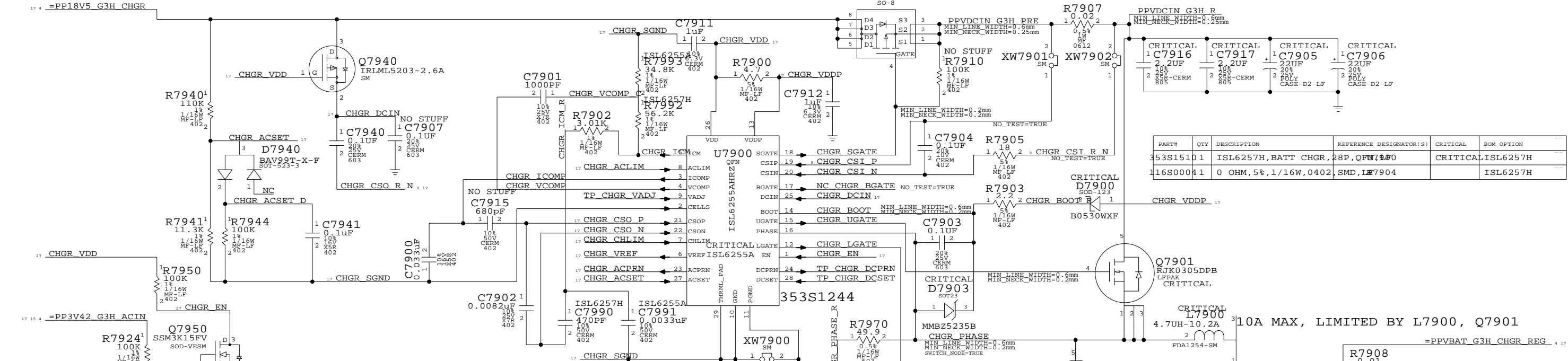
5V/3.3V SUPPLY		
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III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		
SIZE	DRAWING NUMBER	REV.
D	051-7226	A
SCALE	SHT	OF
NONE	16	20



PBus Supply & Battery Charger

Energy Star LDO

Battery Charge FETs



PBus Supply & Batt. Charger

SYNC_MASTER=(Master) SYNC_DATE=(Master)

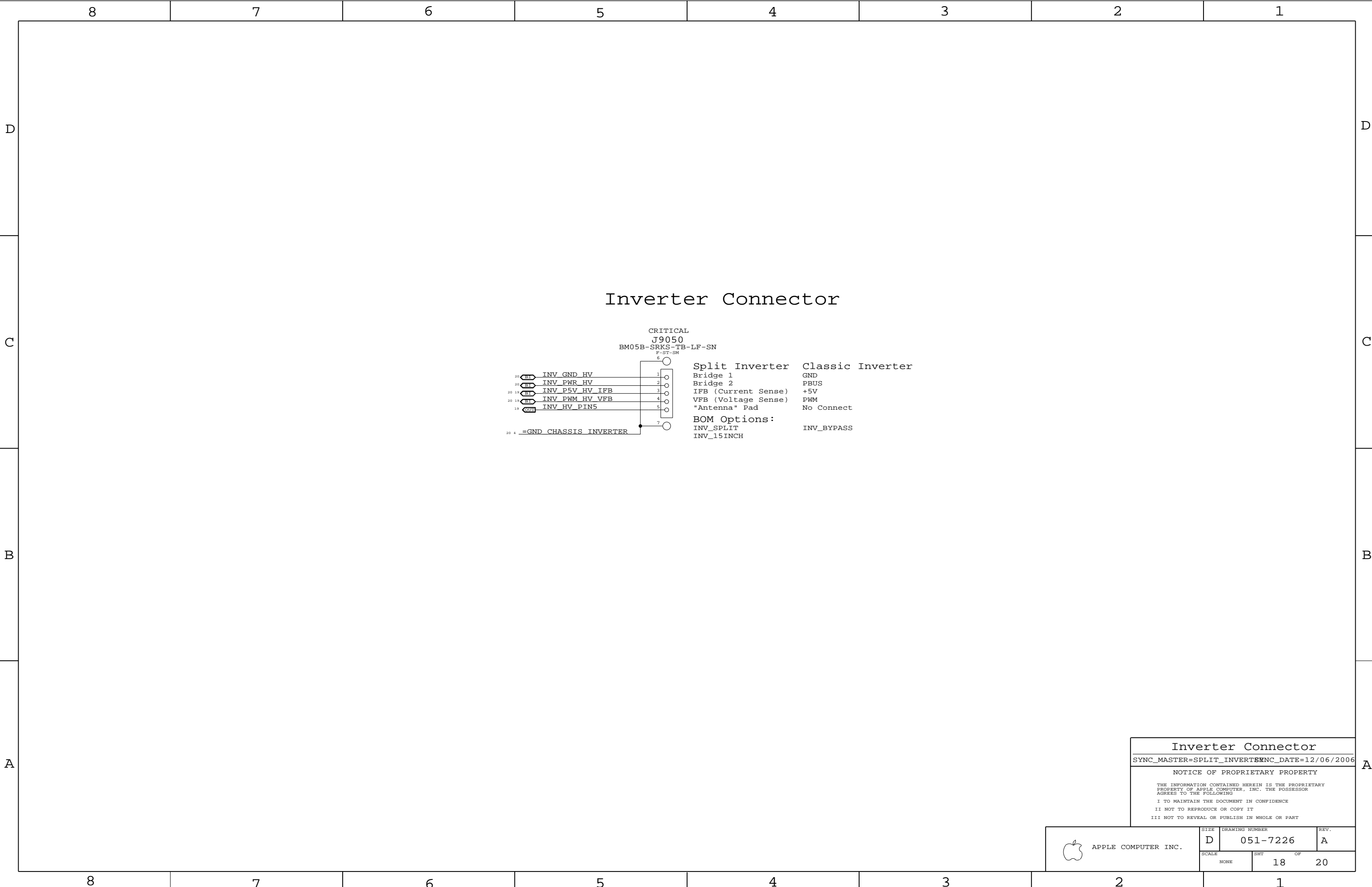
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Inverter Connector

SYNC_MASTER=SPLIT_INVERTERSYNC_DATE=12/06/2006


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SCALE		SHT	OF	
NONE		18	20	

